

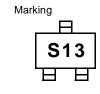
FJX3013R

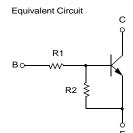
Switching Application (Bias Resistor Built In) - Switching circuit, Inverter, Interface circuit, Driver Circuit

- Built in bias Resistor (R₁ =2.2K Ω , R₂=47K Ω)
- Complement to FJX4013R



1. Base 2. Emitter 3. Collector





NPN Epitaxial Silicon Transistor

Absolute Maximum Ratings T_a=25°C unless otherwise noted

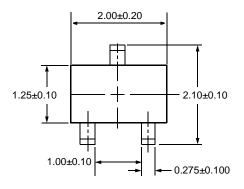
Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	50	V
V _{CEO}	Collector-Emitter Voltage	50	V
V_{EBO}	Emitter-Base Voltage	10	V
С	Collector Current	100	mA
Pc	Collector Power Dissipation	200	mW
Γ _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

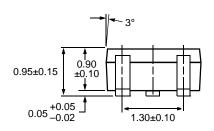
Electrical Characteristics T_a=25°C unless otherwise noted

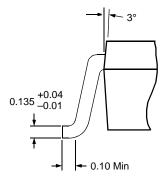
Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	I _C =10μA, I _E =0	50			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C =100μA, I _B =0	50			V
I _{CBO}	Collector Cut-off Current	V_{CB} =40V, I_{E} =0			0.1	μΑ
h _{FE}	DC Current Gain	V _{CE} =5V, I _C =5mA	68			
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C =10mA, I _B =0.5mA			0.3	V
f _T	Current Gain Bandwidth Product	V _{CE} =10V, I _C =5mA		250		MHz
C _{ob}	Output Capacitance	V _{CB} =10V, I _E =0 f=1.0MHz		3.7		pF
V _I (off)	Input Off Voltage	V _{CE} =5V, I _C =100μA	0.5			V
V _I (on)	Input On Voltage	V_{CE} =0.2V, I_{C} =5mA			1.1	V
R ₁	Input Resistor		1.5	2.2	2.9	ΚΩ
R ₁ /R ₂	Resistor Ratio		0.042	0.047	0.052	

Package Dimensions

SOT-323







Dimensions in Millimeters

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CROSSVOLT™	FRFET™	MicroPak™	QFET™	SuperSOT™-8
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Programmable Ad	ctive Droop™	OPTOPLANAR™	SMART START™	

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